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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jiong-Ping Lu
Serial No: 10/790,606
Examiner: Cuong Quang Nguyen
Filed: 03/01/2004
For: SEMICONDUCTOR DEVICE HAVING A SILICIDED GATE ELECTRODE AND METHOD OF MANUFACTURE THEREFOR

Docket No: TI-37479
Conf. No: 9593
Art Unit: 2811

ELECTION

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 12-14-04.

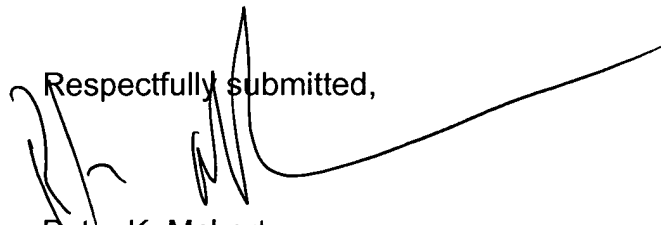

Ann Trent

Dear Sir:

This election is offered in response to the Examiner's restriction requirement mailed November 17, 2004.

Applicant hereby elects to pursue Group II of Claims 10-19, drawn to a method of making a semiconductor device, without traversing the Examiner's restriction requirement.

Respectfully submitted,



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